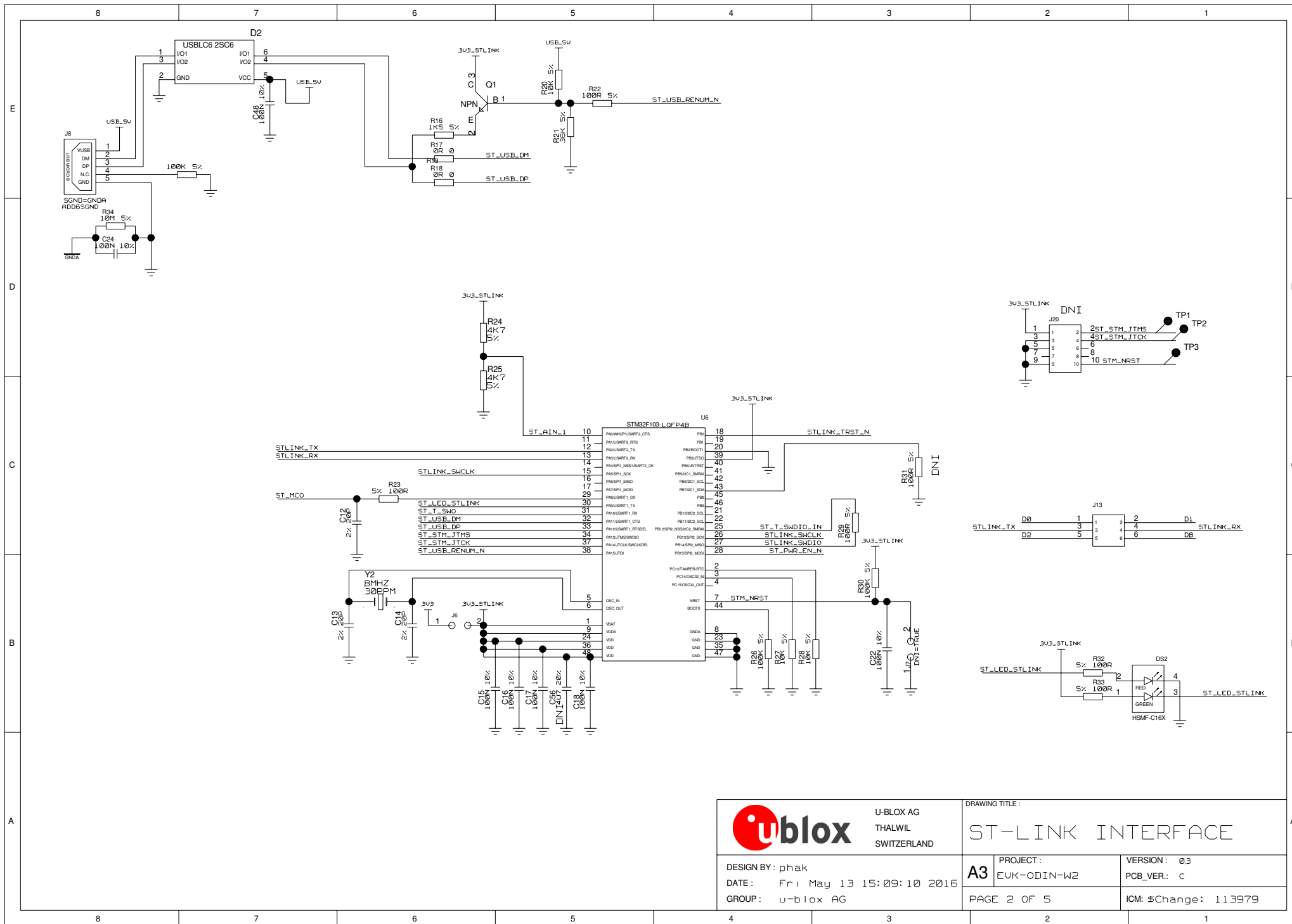

	U-BLOX AG THALWIL SWITZERLAND		DRAWING TITLE: HEADERS/LED/SW	
	DESIGN BY: phak DATE: Fri May 13 15:09:10 2016 GROUP: u-blox AG	A3 PROJECT: EUK-ODIN-W2	VERSION: 03 PCB_VER.: c	PAGE 1 OF 5 ICM: \$Change: 11,3979

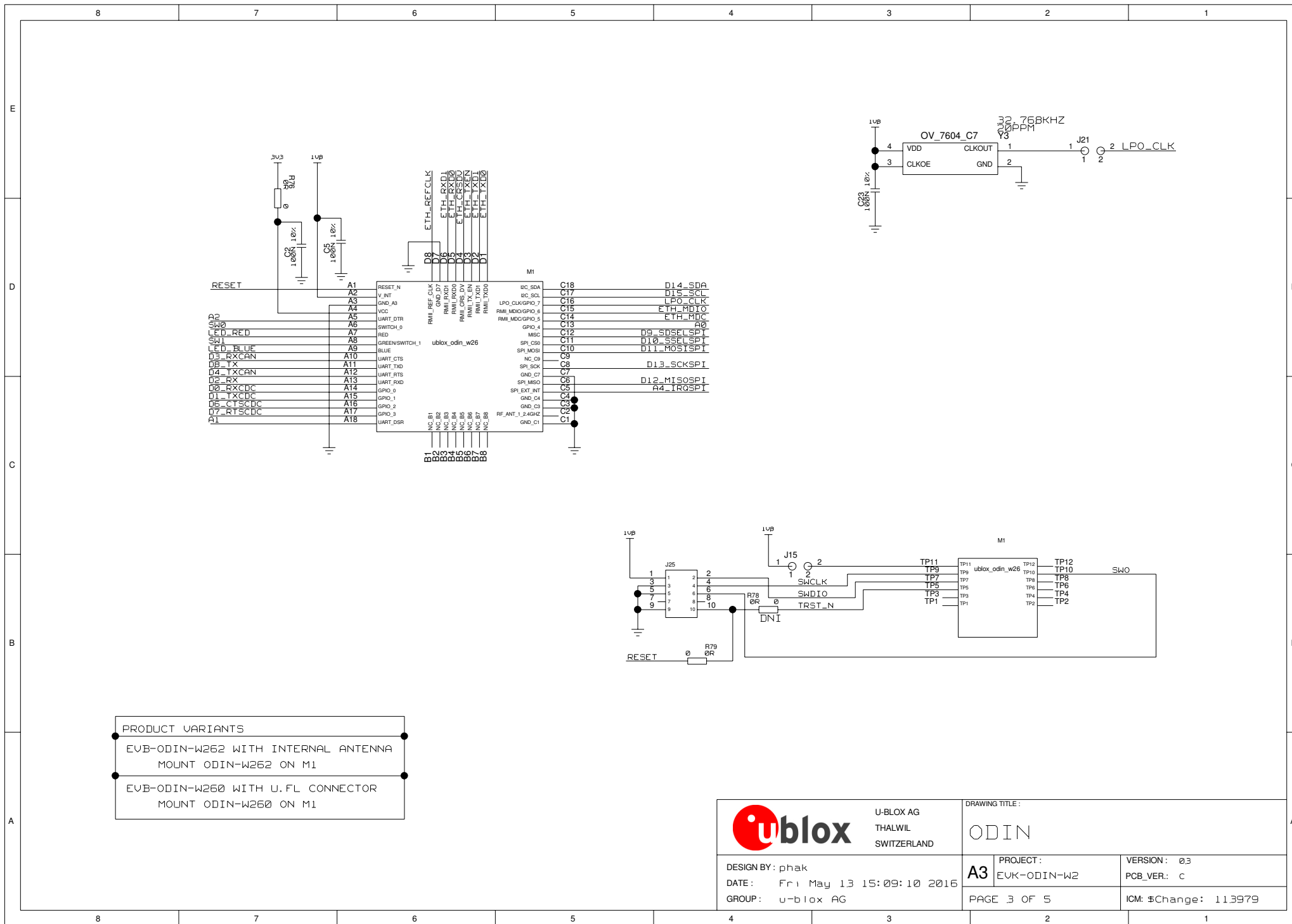




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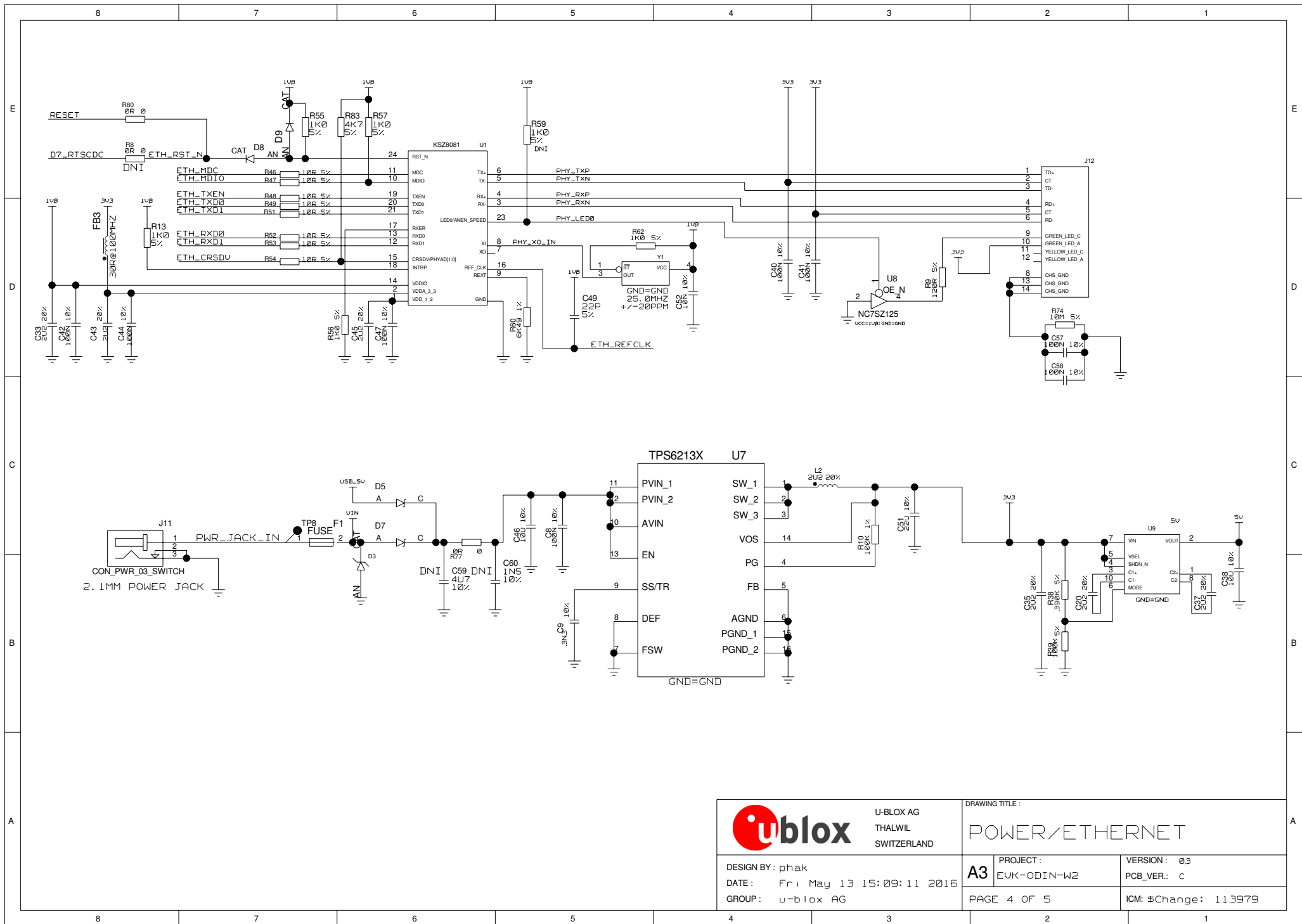
DESIGN BY: phak
 DATE: Fri May 13 15:09:10 2016
 GROUP: u-blox AG


DRAWING TITLE:
ST-LINK INTERFACE

A3	PROJECT: EUK-ODIN-W2	VERSION: 03 PCB_VER.: c
	PAGE 2 OF 5	ICM: \$Change: 11.3979




 U-BLOX AG THALWIL SWITZERLAND	DRAWING TITLE:	
	ODIN	
DESIGN BY: phak	PROJECT:	VERSION: 03
DATE: Fri May 13 15:09:10 2016	A3 EVK-ODIN-W2	PCB_VER.: c
GROUP: u-blox AG	PAGE 3 OF 5	ICM: \$Change: 11.3979



 U-BLOX AG THALWIL SWITZERLAND	DRAWING TITLE:	
	POWER/ETHERNET	
DESIGN BY: phak DATE: Fri May 13 15:09:11 2016 GROUP: u-blox AG	PROJECT: A3 EUK-ODIN-W2	VERSION: 03 PCB_VER.: c
PAGE 4 OF 5		ICM: \$Change: 11.3979

REVISION HISTORY			
REV	SHEET	DESCRIPTION	DATE/DESIGNER
01	ALL	FIRST VERSION OF C029	RUST
02	ALL	CONCEPT AND DESIGN UPDATE OF C029	2015-12-14/PHAK
03	ALL	ST-LINK - ODIN-W2 RESET CHANGED, TEST POINTS NOT USED AS GPIO	2016-05-11/PHAK

	U-BLOX AG THALWIL SWITZERLAND		DRAWING TITLE: REVISION HISTORY	
	DESIGN BY: phak DATE: Wed May 11 10:09:02 2016 GROUP: u-blox AG	A3	PROJECT: EVK-ODIN-W2	VERSION: 03 PCB_VER.: c
	PAGE 5 OF 5		ICM: \$Change: 113979	